

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3151004	memory or storage	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:32
L2	299993	1 and (dynamic or \$2dram or filed adj programmable or fpga)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:33
L3	13500	2 and capacitor with (bit or bitline or column)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:40
L4	3287	3 and (select\$5 or switch\$5 or transistor) with (drain or source) with (word or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:41
L5	3710	3 and (select\$5 or switch\$5 or transistor) with (drain or source) with (word or wordline or row)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:41
L6	1642	5 and capacitor near4 (electrode or node) with (switch\$5 or transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:42
L7	1314	6 and (transistor or switch\$5 or select\$5) with (source or drain) with (electrode or node or plate) with capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:40
L8	1040	7 and (cell or unit) with capacitor with (bit or bitline or column)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:40
L9	695	8 and (cell or unit) with (select\$5 or switch\$5 or transistor) with (drain or source) with (word or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:41
L10	695	9 and (cell or unit) with (select\$5 or switch\$5 or transistor) with (drain or source) with (word or wordline or row)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:42
L11	564	10 and (cell or unit) with capacitor near4 (electrode or node) with (switch\$5 or transistor)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:43

L12	325	11 and (cell or unit) with capacitor with (plate or electrode or node) near3 capacitor with (source or drain) with (bit or bitline) with (drain or source) with (word or wordline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:45
L13	292	12 and (cell or unit) with capacitor near3 (plate or node or electrode) with (gate or control\$5) with (transistor or switch\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:47
L14	283	13 and (cell or unit) with capacitor with (connect\$5 or coupled) with (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:48
L15	217	14 and gate with (connect\$5 or connect\$5) near5 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:49
L16	118	14 and gate near4 (connect\$5 or connect\$5) near5 (bit or bitline)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:50
L17	114	16 and (source or drain) with (connect\$5 or coupl\$5) with (electrode or plate or node) near3 capacitor	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/06/27 09:51